



## Understanding the Reliability Physics of Electronic and Photonic Products: Roles of Failure-Oriented-Accelerated-Tests (FOATS)

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The reliability bottleneck of electronic, photonic, MEMS or MOEMS (optical MEMS) systems is, as is known, the physical performance of their materials and structures [1] and not its functional (electrical or optical) performance. It is well known also that it is the device packaging that is the most critical undertaking, when making a viable, properly protected and effectively-interconnected device and package into a reliable product. Accelerated life testing (ALT) [2,3] conducted at different stages of an IC package design and manufacturing is the major means for achieving that. Burn-in-testing (BIT) [4], the chronologically final ALT, aimed at eliminating the infant mortality portion of the bathtub curve prior to shipping to the customer(s) the “healthy” products, i.e. those that survived BIT, is particularly important: BIT is an accepted practice for detecting and eliminating possible early failures in the just fabricated products. Originally BIT used continuously powering the manufactured products by applying elevated temperature to accelerate their aging, but today various stressors, other-than-elevated-temperature, or a physically meaningful combination of several of them, are employed in this capacity. BIT, as far as “freaks” are concerned, is and always was, of course, a failure-oriented-accelerated-testing (FOAT) type of testing. The recent analyses improved our understanding of the role and significance of several important factors that affected BIT’s testing time and stress level: the random failure rate of the mass-produced components that the product of interest is comprised of; the way to assess, from the highly focused and highly cost effective FOAT, the activation energy of the “freak” population; the role of the applied stressor(s); and, most importantly, - the probabilities of the “freak” population failures depending on the duration and

level of the BIT’s effort. These factors should be considered when there is an intent to quantify and, eventually, to optimize the BIT’s procedure. But there is also another, so far less well-known and not always conducted today, FOAT [5-8] that has been recently suggested in connection with the novel probabilistic design for reliability (PDfR) concept [9-12]. Such a FOAT is supposed to be conducted at the design stage as a highly focused and highly cost effective undertaking and is the experimental foundation of the PDfR concept. Unlike BIT, which is always a must, the design stage FOAT should be conducted, when developing a new technology or considering a new design, and when there is an intent to better understand the physics of failure and, for many demanding applications, such as, e.g., aerospace, military, or long-haul communications, to quantify the lifetime and the corresponding, in effect, never-zero probability of failure of the product in the field. Such a design-stage FOAT could be viewed as a quantified and reliability-physics-oriented forty years old highly-accelerated-life-testing (HALT) [13,14], and should be particularly recommended for new technologies and new designs, whose physics of failure is yet unclear and when neither a suitable HALT, nor more or less well established “best practices” exist. When FOAT at the design stage and BIT at the manufacturing stage are conducted, a suitable and physically meaningful constitutive equation, such as, e.g., the kinetic multi-parametric Boltzmann-Arrhenius-Zhurkov (BAZ) model [15-21], could be employed to predict, from the test data, the probability of failure and the corresponding useful lifetime of the product. It is noteworthy that the flexible and physically meaningful BAZ model can be used also to predict the lifetime not only of IC packages, but the lifetime and the corresponding probability of

failure of electronic devices as well [22]. The referenced reliability physics oriented analyses use, as a rule, analytical (“mathematical”) predictive modeling [23,24]. In the author’s opinion and experience, such modeling should always complement computer simulations: these two major modeling tools are based on different assumptions and use different computation techniques, and if the calculated data obtained using these tools are in agreement, then there is a good reason to believe that the obtained data are accurate and trustworthy.

## Bibliography

1. E Suhir. “Microelectronics and Photonics – the Future”. *Microelectronics Journal* 31.11-12 (2000).
2. E Suhir. “Reliability and Accelerated Life Testing”. *Semiconductor International* Feb. 1, (2005).
3. E Suhir and R Mahajan. “Are Current Qualification Practices Adequate?”. *Circuit Assembly* (2011).
4. E Suhir. “To Burn-in, or not to Burn-in: That’s the Question”. *Aerospace* 6.3 (2019).
5. E Suhir. “Failure-Oriented-Accelerated-Testing (FOAT) and Its Role in Making a Viable IC Package into a Reliable Product”. *Circuits Assembly*, July (2013).
6. E Suhir. “Could Electronics Reliability Be Predicted, Quantified and Assured?”. *Microelectronics Reliability*, No. 53, April 15, (2013).
7. E Suhir, *et al.* “Highly Accelerated Life Testing (HALT), Failure Oriented Accelerated Testing (FOAT), and Their Role in Making a Viable Device into a Reliable Product”. 2014 IEEE Aerospace Conference, Big Sky, Montana, March (2014).
8. E Suhir. “Failure-Oriented-Accelerated-Testing (FOAT), Boltzmann-Arrhenius-Zhurkov Equation (BAZ) and Their Application in Microelectronics and Photonics Reliability Engineering”. *International Journal of Aeronautical Science and Aerospace Research (IJASAR)* 6.3 (2019).
9. E Suhir. “Probabilistic Design for Reliability”. *Chip Scale Reviews* 14.6 (2010).
10. E Suhir, *et al.* “Probabilistic Design for Reliability (PdFR) and a Novel Approach to Qualification Testing (QT)”. 2012 IEEE/AIAA Aerospace Conf., Big Sky, Montana, (2012).
11. E Suhir. “Physics of Failure of an Electronics Product Must Be Quantified to Assure the Product’s Reliability”. Editorial, *Acta Scientific Applied Physics* 2.1 (2021).
12. E Suhir. “Remaining Useful Lifetime (RUL): Probabilistic Predictive Model”. *International Journal of Prognostics-and-Health-Monitoring (PHM)* 2.2 (2011).
13. M Silverman. “Forty Years of HALT: What Have We Learned?”. ASTR 2012 Oct 17-19, Toronto, (2012).
14. N Doertenbach. “The Application of Accelerated Testing Methods and Theory (HALT and HASS)”. QualMark Corporation. Archived from the original on 2012-03-01.
15. E Suhir, *et al.* “Technical Diagnostics in Electronics: Application of Bayes Formula and Boltzmann-Arrhenius-Zhurkov Model”. *Circuit Assembly*, Dec. 3, (2012).
16. E Suhir and S Kang. “Boltzmann-Arrhenius-Zhurkov (BAZ) Model in Physics-of-Materials Problems”. *Modern Physics Letters B (MPLB)* 27 (2013).
17. E Suhir and A Bensoussan. “Application of Multi-Parametric BAZ Model in Aerospace Optoelectronics”. 2014 IEEE Aerospace Conference, Big Sky, Montana, March (2014).
18. E Suhir. “Three-Step Concept in Modeling Reliability: Boltzmann-Arrhenius-Zhurkov Physics-of-Failure-Based Equation Sandwiched Between Two Statistical Models”. *Microelectronics Reliability* Oct. (2014).
19. E Suhir. “Static Fatigue Lifetime of Optical Fibers Assessed Using Boltzmann-Arrhenius-Zhurkov (BAZ) Model”. *Journal of Materials Science: Materials in Electronics* 28.16 (2017).
20. A Ponomarev and E Suhir. “Predicted Useful Lifetime of Aerospace Electronics Experiencing Ionizing Radiation: Application of BAZ Model”. *Journal of Aerospace Engineering and Mechanics (JAEM)* 3.1 (2019).
21. E Suhir. “Boltzmann-Arrhenius-Zhurkov Equation and Its Applications In Electronic-and-Photonic Aerospace Materials Reliability-Physics Problems”. *International Journal of Aeronautical Science and Aerospace Research (IJASAR)*, March 24, (2020).
22. E Suhir and Z Stamenkovic. “Using Yield to Predict Long-Term Reliability of Integrated Circuit (IC) Devices: Application of Boltzmann-Arrhenius-Zhurkov Model”. *Solid-State Electronics* 164 (2020).

23. E Suhir. "Analytical Modeling in Electronic Packaging Structures: Its Merits, Shortcomings and Interaction with Experimental and Numerical Techniques". *ASME Journal of Electronic Packaging* 111, June (1989).
24. E Suhir. "Analytical Thermal Stress Modeling in Electronic and Photonic Systems". *ASME Applied Mechanics Reviews* 62.4 (2009).